

<b>Notice of References Cited</b>	Application/Control No. 10/082,318	Applicant(s)/Patent Under Reexamination IKEDA ET AL.	
	Examiner Donghee Kang	Art Unit 2811	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,472,244 B1 ✓	10-2002	Ferrari et al.	438/53
	B	US-5,357,807 A ✓	10-1994	Guckel et al.	73/721
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP 409257618A /	10-1997	Japan	Shimaoka et al.	
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S>Wolf, "Silicon Processing for the VLSI Era, Vol.1, pp564.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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